

Attorney Docket No: 042390.P12542C

Patent

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re Application of:  P. Patra, et al.              |   |
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| )  | Examiner: Unknown   |
| Application No.: 10/665,521 )                        | Art Unit: 2825  |
| Filing Date: September 22, 2003 )                    | I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner |
| For: Time Synthesis for Power Optimization of High ) | for Patents, PO Box 1450, Alexandria, Virginia 22313-1450   |
| Performance Circuits                                 | on 3/10/04  |
| Commissioner for Patents                             | Debral. Milaume   |
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| Alexandria, Virginia 22313-1450                      | My and Mulbrune 3110104   |
| . •  | Signature Date  |

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The assignee, Intel Corporation, of the above-identified Patent Application, hereby revokes all previous powers of attorney given in this Patent Application, and appoints the firm identified below and individual.

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The individual whose signature appears below is authorized to execute this Power of Attorney on behalf of Intel Corporation.

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Date: 1/1000/10, 2004

Pam Matlock Secretary of Patents

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